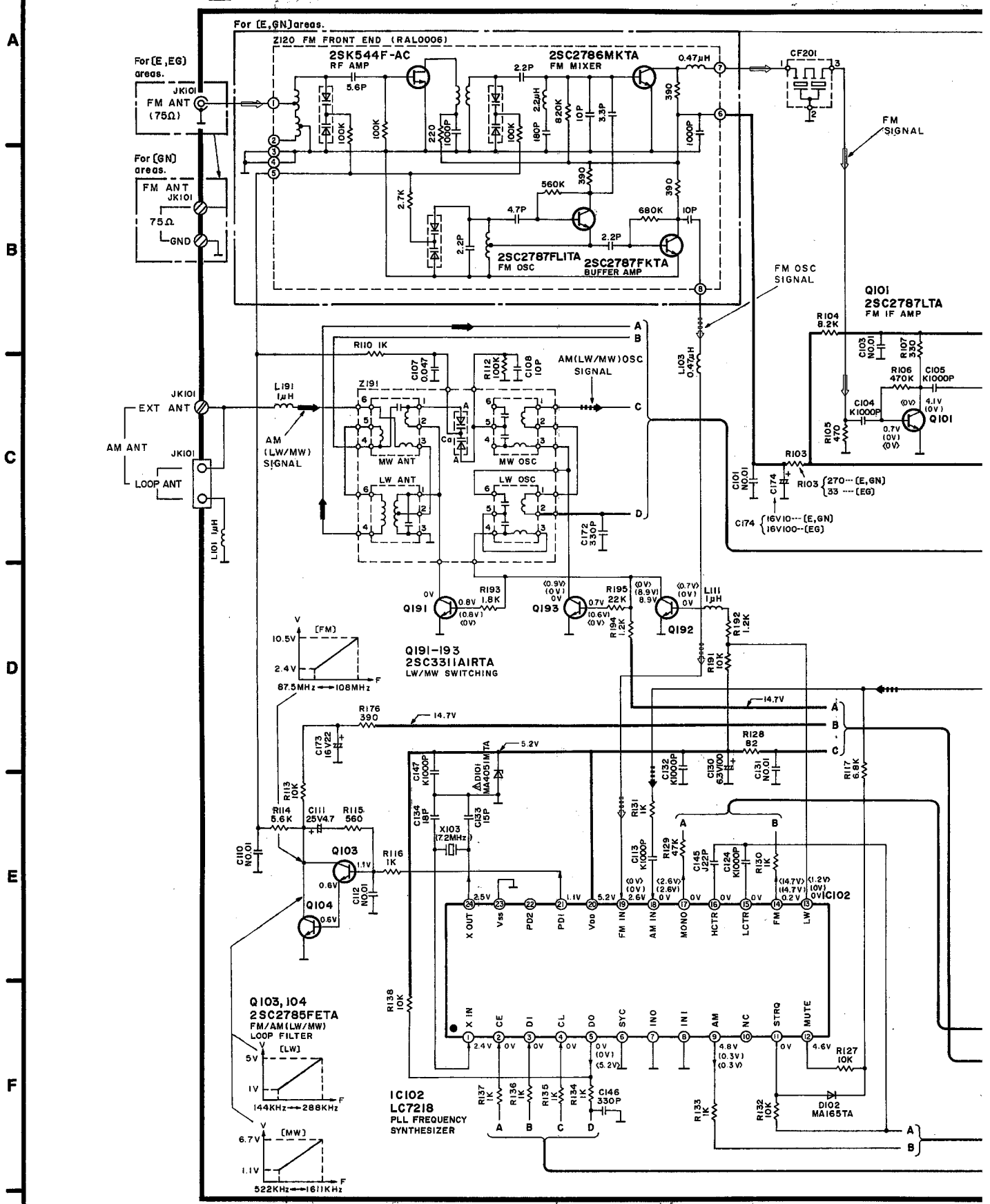


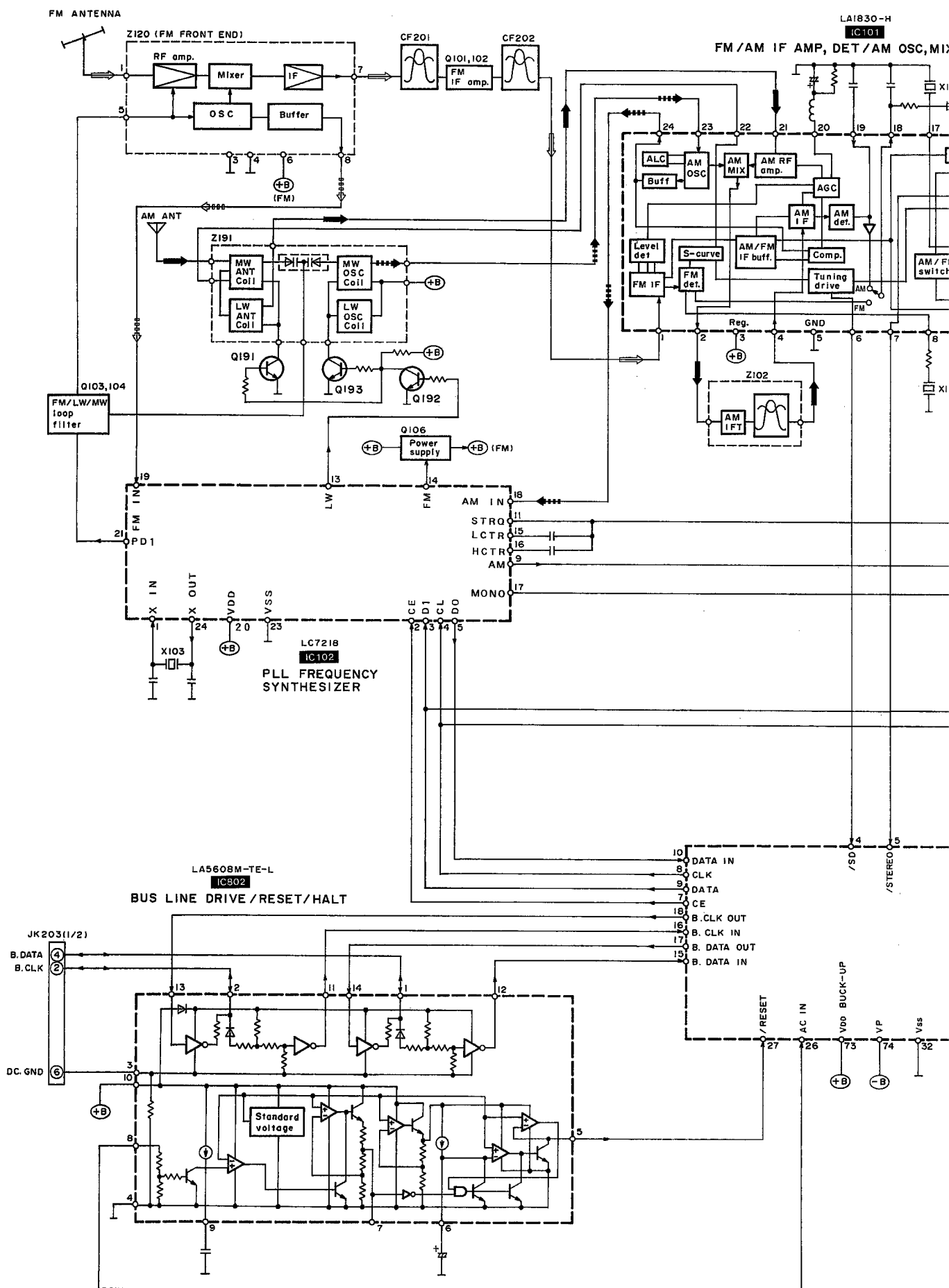
Schematic Diagram • Tuner circuit (Parts list on pages 23~27)

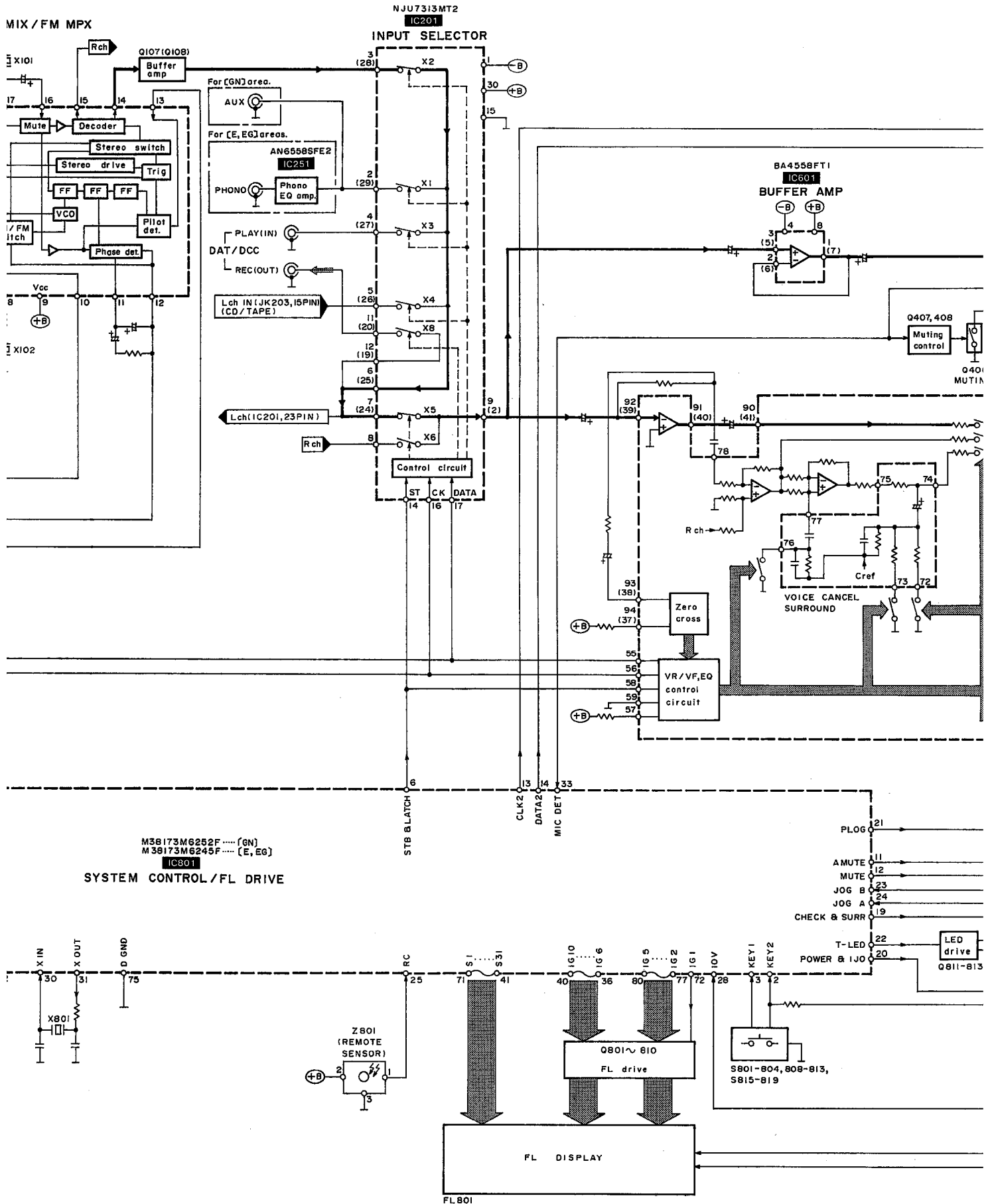
• Indicated voltage values are the standard values.
Therefore, there may exist some error.

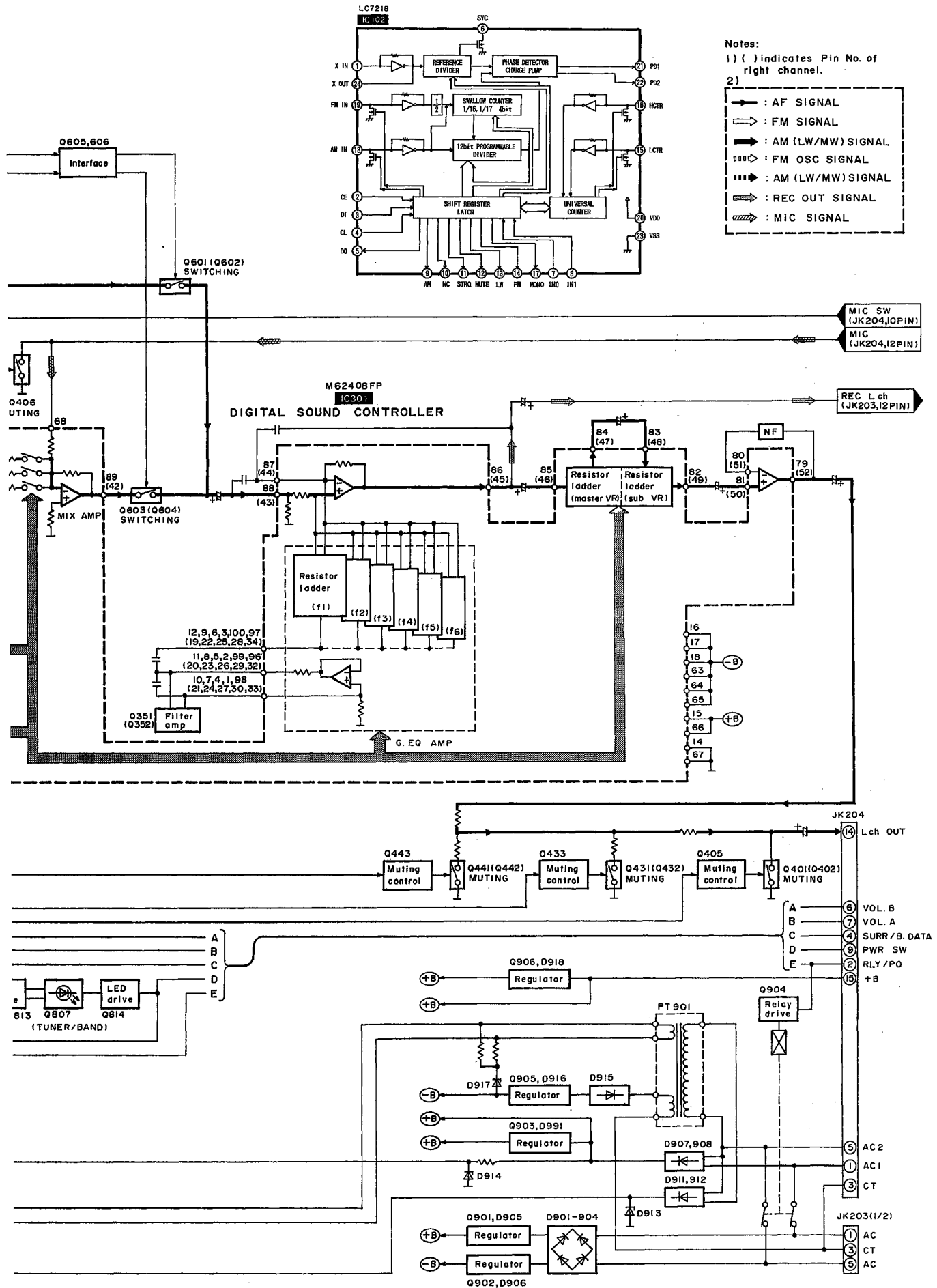
A TUNER CIRCUIT



■ Block Diagram







————— : AM (LW/MW) signal line

————— : FM signal line

————— : Positive voltage line

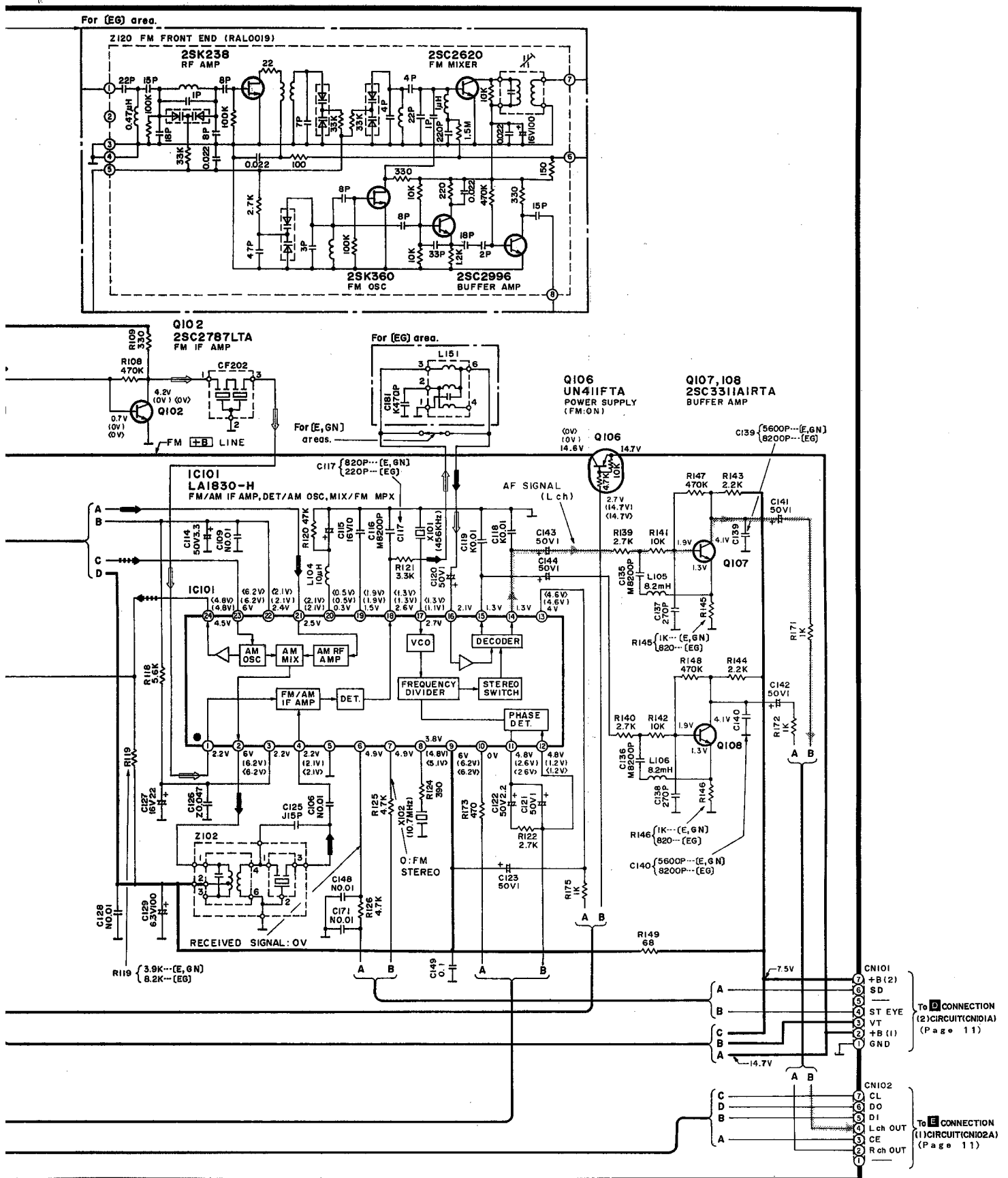
■■■■■ : AM (LW/MW) OSC signal line

□□□□ : FM OSC signal line

■■■■■ : AF signal line

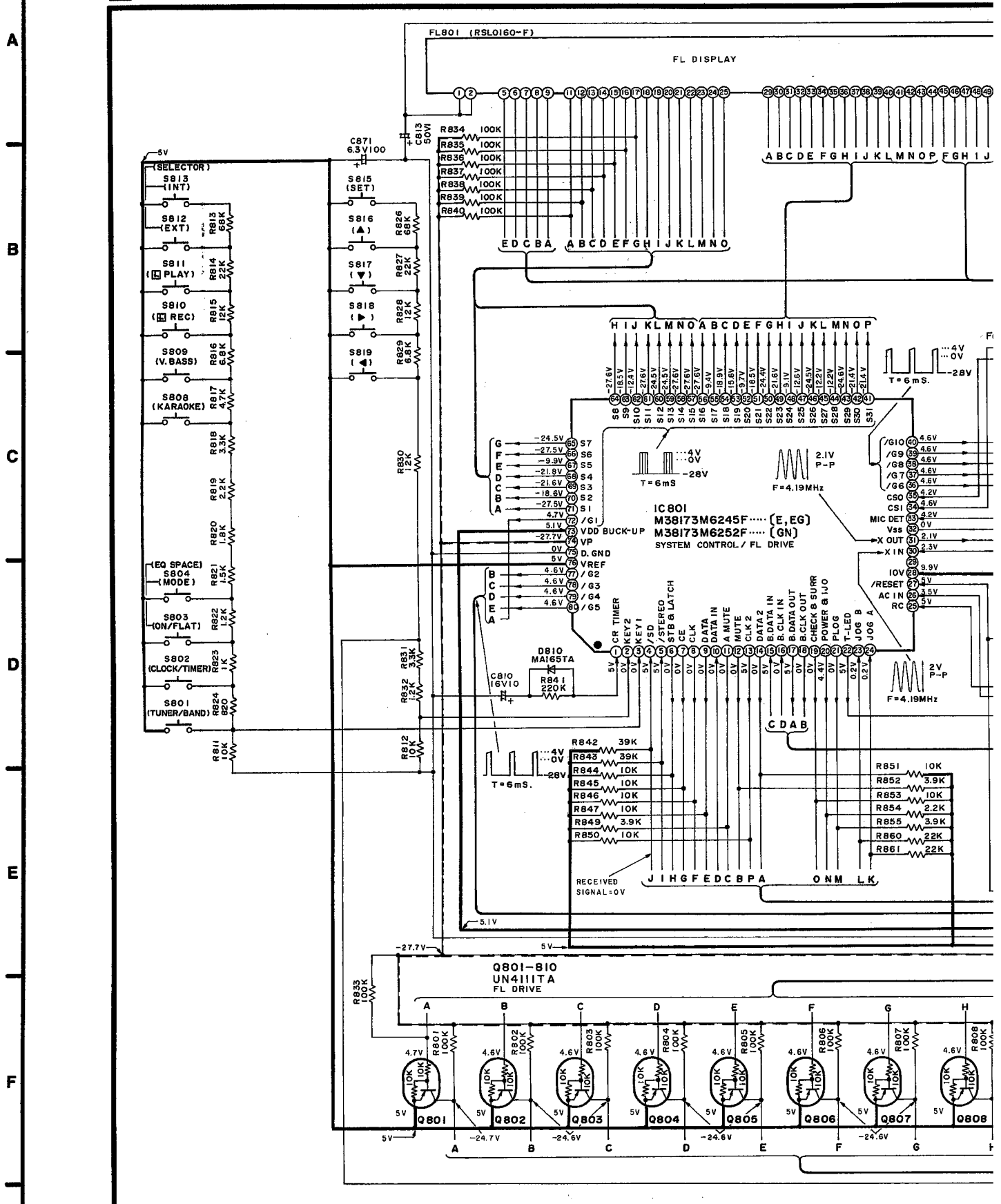
5 6 7 8 9

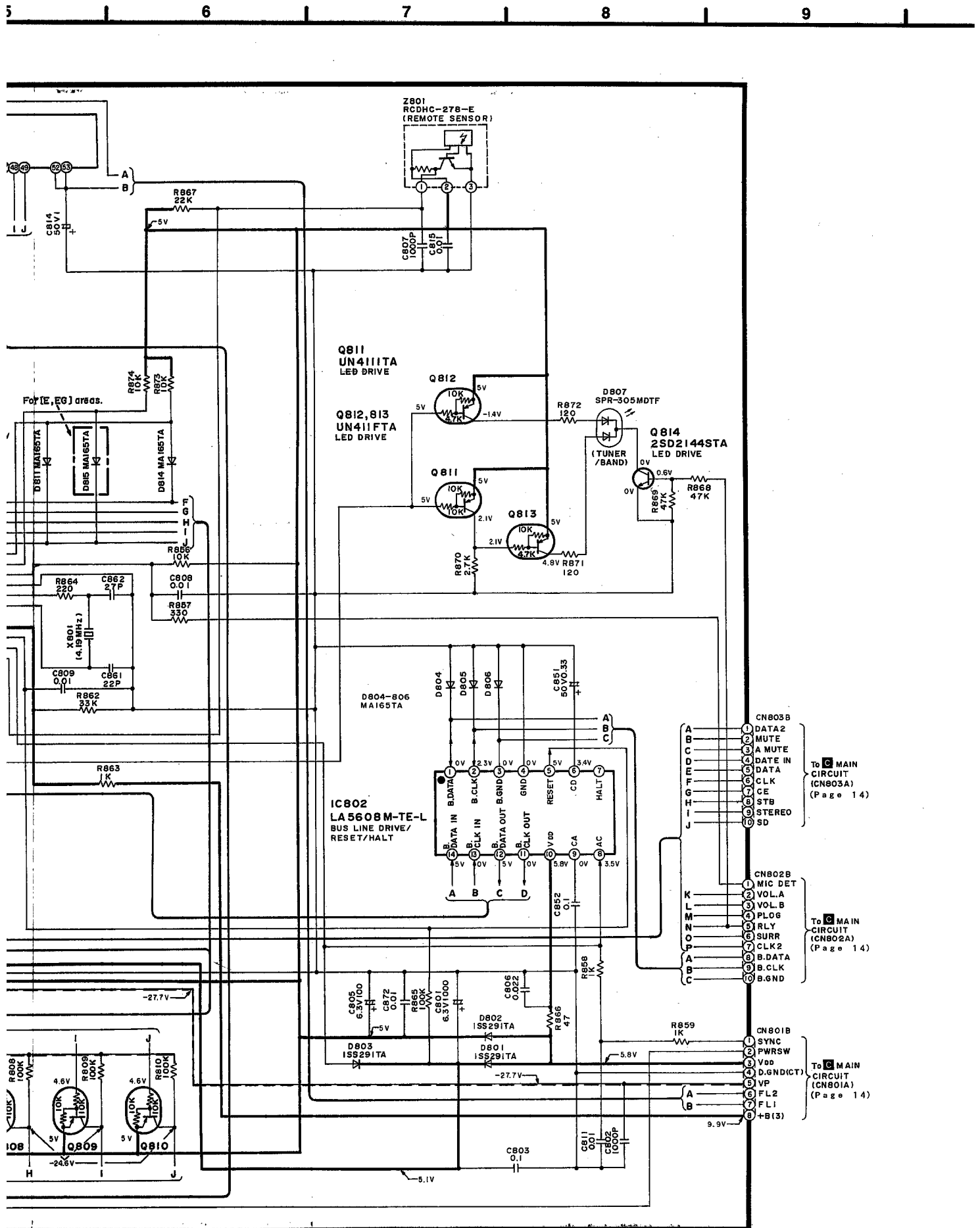
Standard values for the unit measured by the DC electronic circuit tester (high-impedance) with the chassis taken as standard.
 Errors in the voltage values, depending on the internal impedance of the DC circuit tester. No mark: FM mode (): MW mode < >: LW mode



Schematic Diagram • Operation circuit (Parts list on pages 23~27)

B OPERATION CIRCUIT





A



— : Positive voltage line  : Rf
 - - - : Negative voltage line  : MI

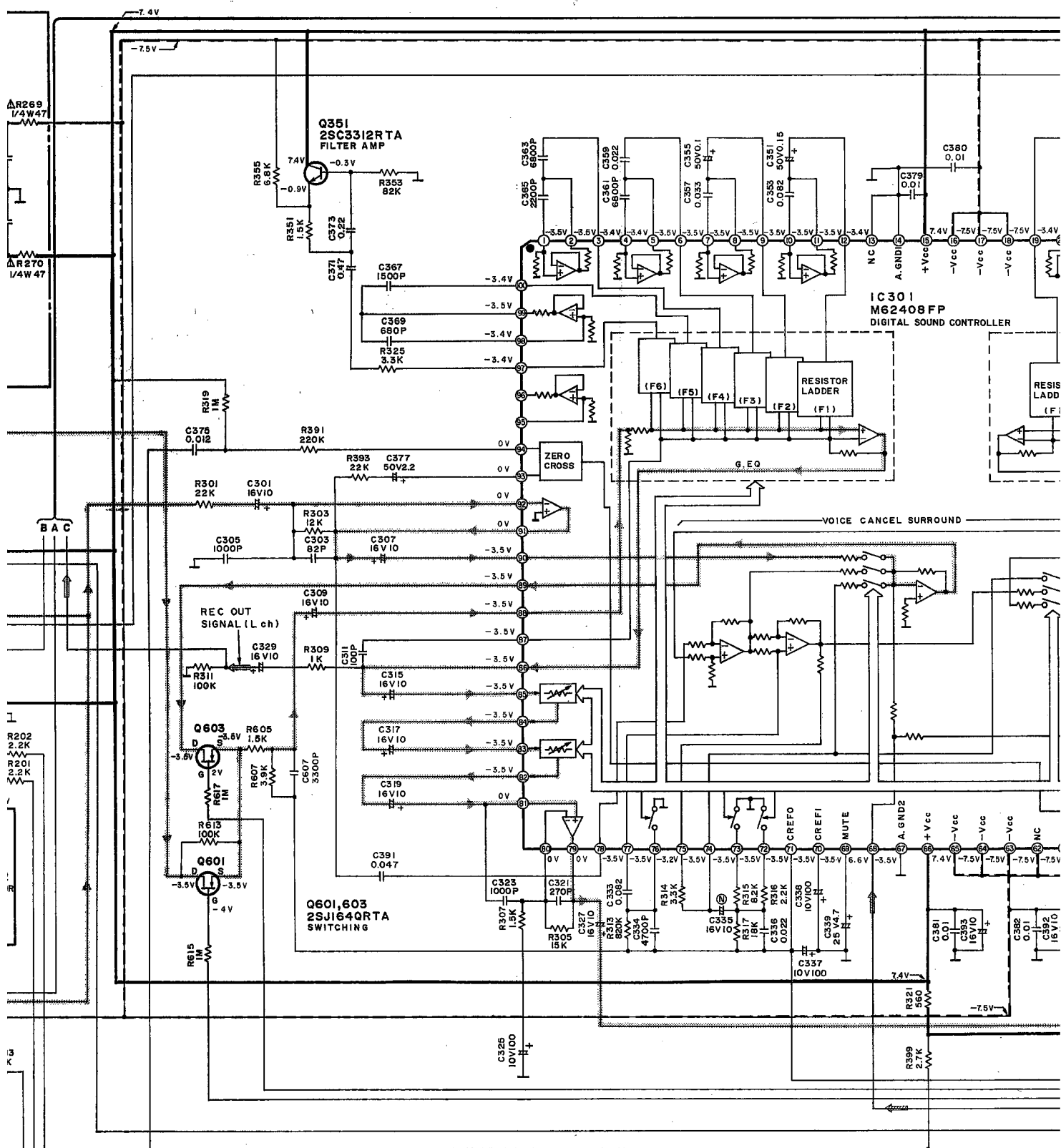
6

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8

9

/BUFFER AMP/MUTING/REGULATOR)



: REC. OUT signal line : AF signal line

: MIC signal line

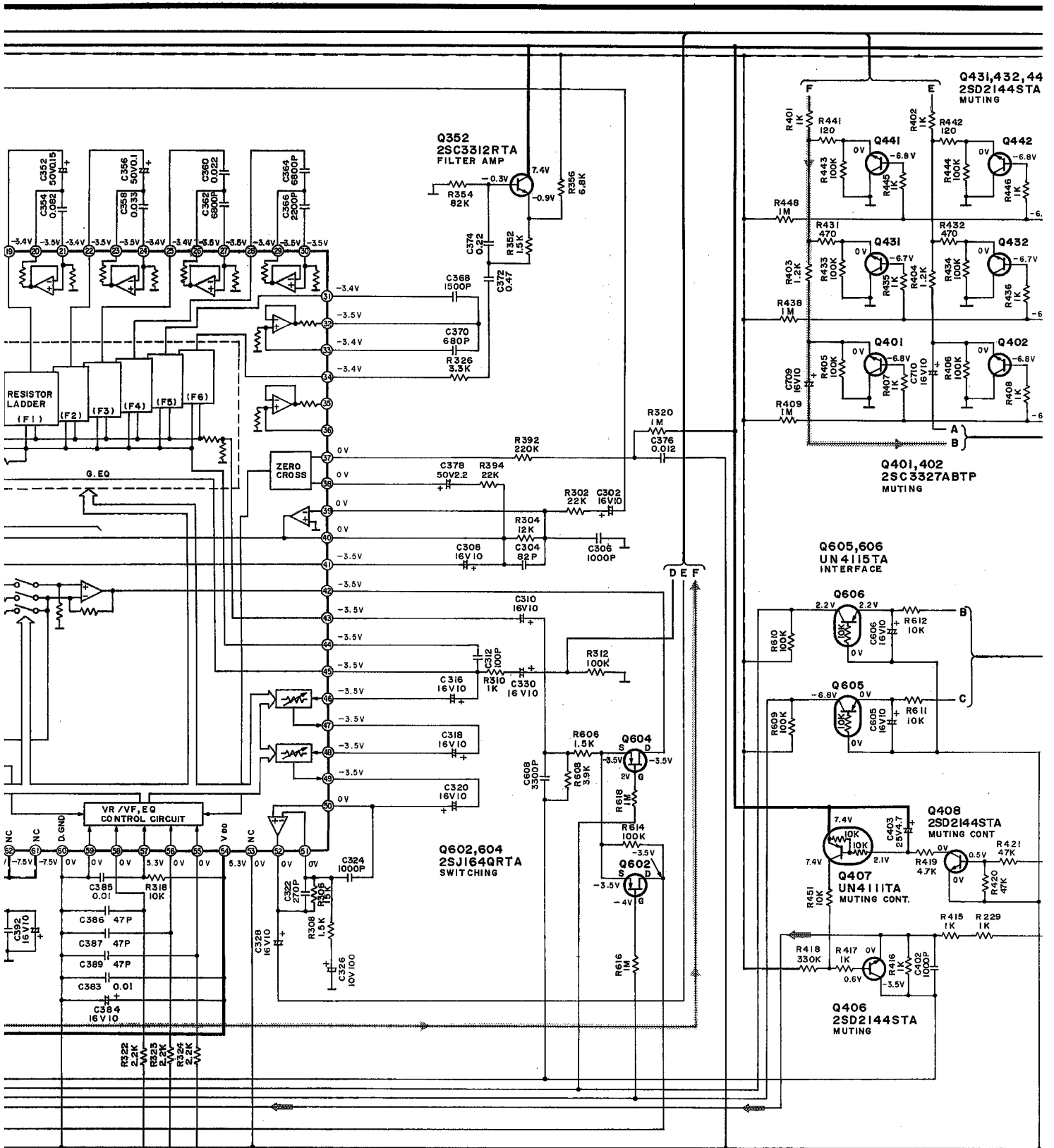
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